



Event Update

DEAR IEEE AUSTRIA SECTION MEMBERS,

besides an update of the upcoming events, we would like to introduce two of our IEEE Austria Section members in this newsletter: Vice Chair Werner Drexel (Infineon) and Chapter Coordinator Daniel Watzenig (TU Graz).

Vice Chair IEEE Austria Section

DI Werner Drexel received a master's degree in Communications from the Technical University Graz. He worked afterward as freelance for a semiconductor company with focus on wired data communication. Since 1998, he works for Infineon Technologies in the Chip Card business unit at the beginning as analog designer for hardware security sensors where he held several patents. In the last ten years, the focus was on hardware security concepts. In the last years the activities expanded to data security and privacy. Focus is on government identification and payment but also on public transport ticketing. He works also for the EPPC (European Public Policy Committee) Cybersecurity and EPPC Intelligent, Connected and Autonomous Vehicles. Past in IEEE: Werner has been the chapter coordinator for Section Austria since 2014 and the secretary of the Austrian Intelligent Transportation Systems chapter. He is member of the IEEE SA several working groups with the aspect of privacy and security (actual in P1912 Standard for Privacy and Security Framework for Consumer Wireless Devices and IEEE P2784 Smart Cities Planning and Technology)



**CHAPTER
COORDINATOR IEEE**

AUSTRIA SECTION

Daniel Watzenig was born in Austria. He received his doctoral degree in electrical engineering from Graz University of Technology, Austria, in 2006. In 2009 he received the *venia docendi* for Electrical Measurement and Signal Processing. Since 2008 he is Divisional Director of the Automotive Electronics Department at the Virtual Vehicle Research Center Graz. In 2017 he was appointed as Full Professor of Autonomous Driving at the Institute of Automation and Control, Graz University of Technology, Austria. His research interests focus on sense and control of automated vehicles, sensor fusion, and uncertainty estimation. He is author or co-author of over 180 peer-reviewed papers, book chapters, patents, and articles. He is Editor-in-Chief of the SAE Int. Journal on Connected and Automated Vehicles (SAE JCAV, launched in 2018). Since 2019 he is invited guest lecturer at Stanford University, USA, teaching multi-sensor perception, data fusion, and software for autonomous systems (Principles of Robot Autonomy I). He is founder of the Autonomous Racing Graz Team, one of currently six teams of the global Roborace race series. Access further information about Daniel via <https://iee-ims.org/contact/daniel-watzenig>.



IEEE SECTION AUSTRIA IS SUPPORTING STUDENT ACTIVITIES

The IEEE Austria Section is supporting students in enhancing their career and setup innovative events together with their study colleagues by offering two award programmes specifically designed for them. The **IEEE Austria Student Mobility and Conference Grant** support section student members to participate at conferences, workshops and other educational activities in the fields of IEEE and related. The goal is to assist applicants to advance their education, make international contacts and gain additional experience.

Deadlines for submissions are 1st of February, 1st of June and 1st of September each year.

The IEEE Austria Student Branch Award encourages innovative new concepts applied by the Student Branches and activities or events creating positive impact for students. If students created some cool new events or did something special they can contact the local student branch or the student branch coordinator . They will be happy to integrate their efforts and apply in one of the categories of the Student Branch Award.

Deadline for submission is the 15th of September .

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AWARDS

APPLY FOR THE IEEE SECTION AUSTRIA STUDENT AWARDS

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Conferences

AUSTROCHIP 2021

29th Austrian Workshop on Microelectronics

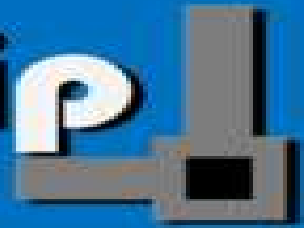
Thursday, 14. October 2021

Location: Johannes Kepler University Linz (JKU), Linz, Austria

Austrochip is an annual meeting and platform to present the latest activities in the field of microelectronics and integrated circuits in Austria and neighboring countries. The workshop is a forum for discussion and contact between academia and industry.

Austrochip

Workshop on Microelectronics



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JC&S Symposium 2022

2nd IEEE International Symposium on Joint Communications & Sensing (JC&S)

From Wednesday, 9. March 2022

To Thursday, 10. March 2022

Location: Congress Center Seefeld, Innsbruck, Austria

The integration of communication and radio sensing in the same spectrum is likely to be one of the key features in B5G wireless systems. Therefore, Joint Communications and Sensing (JC&S) is the subject of rapidly growing interest in the research community, and this symposium will focus on the different system and implementation aspects of this promising new approach, bringing together academic and industrial experts from both the radar and communications research communities.

After being virtual-only in 2021, the event returns in 2022 with a hybrid event, allowing in-person exchange for those who are able to attend in lovely Innsbruck, Austria. For those unable to attend, all sessions will also be streamed online.

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ESIT 2022

IEEE European School of Information Theory (ESIT)

From Sunday, 3. July 2022

To Thursday, 7. July 2022

Location: Technical University of Vienna (TU Wien), Austria

The European School of Information Theory (ESIT) is an annual educational event, organized by the IEEE Information Theory Society (ITSoc), for graduate students, postdocs and researchers from institutes throughout Europe and beyond.

In 2022, the school will be held in-person (provided circumstances allow) in Vienna, Austria, featuring several plenary talks, attendee poster sessions and social events.

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MEETINGS

IEEE Austria Section General Assembly 2021

Thursday, 4. November 2021, 17:00 - 19:00

Location: Virtual Event (online)

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